

## GD1550-SFP-LC.S80/120

**Single-Mode 1550nm 1.25Gbps FC/GBE  
Duplex SFP Transceiver  
RoHS6 Compliant**



### Features

- ◆ Operating Data Rate up to 1.25Gbps
- ◆ 1550nm DFB Laser Transmitter
- ◆ 80km with 9/125 µm SMF

120km with 9/125 µm SMF

Single 3.3V Power Supply  
and LVTTL Logic Interface

- ◆ Hot-Pluggable SFP Footprint Duplex LC Connector Interface
- ◆ Class 1 FDA and IEC60825-1 Laser Safety Compliant
- ◆ Operating Case Temperature

Standard: 0°C~+70°C

Industrial: -40°C~+85°C

- ◆ Compliant with SFP MSA Specification
- ◆ Compliant with SFF-8472

### Applications

- ◆ Gigabit Ethernet Switches and Routers
- ◆ Fiber Channel Switch Infrastructure
- ◆ Other Optical Links

### Ordering Information

Part No.	Data Rate	Fiber	Distance *(note2)	Interface	Temp.	DDMI
GD1550-SFP-LC.S80	1.25Gbps	SMF	80km	LC	0°C~+70°C	YES
GD1550-SFP-LC.S80(WT)	1.25Gbps	SMF	80km	LC	-40°C~+85°C	YES



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GD1550-SFP-LC.S120	1.25Gbps	SMF	120km	LC	0°C~+70°C	YES
GD1550-SFP-LC.S120(WT)	1.25Gbps	SMF	120km	LC	-40°C~+85°C	YES

Note1: Standard version

Note2: 80km/120Km with 9/125 μm SMF

\*The product image only for reference purpose.

### Regulatory Compliance\*Note3

Product Certificate	Certificate Number	Applicable Standard
TUV	R50135086	EN 60950-1:2006+A11+A1+A12+A2
		EN 60825-1:2014
		EN 60825-2:2004+A1+A2
UL	E317337	UL 60950-1
		CSA C22.2 No. 60950-1-07
EMC CE	AE 50285865 0001	EN 55022:2010
		EN 55024:2010
FCC	WTF14F0514417E	47 CFR PART 15 OCT., 2013
FDA	/	CDRH 1040.10
ROHS	/	2011/65/EU

Note3: The above certificate number updated to June 2014, because some certificate will be updated every year, such as FDA and ROHS. For the latest certification information, please check with us.

### Product Description

GD1550-SFP-LC.S80,120 series single-mode transceiver is small form factor pluggable module for serial optical data communications such as Gigabit Ethernet 1000BASE-ZX and Fiber Channel 1x SM-LC-L FC-PI. It is with the SFP 20-pin connector to allow hot plug capability. This module isdesigned for single mode fiber and operates at a nominal wavelength of 1550nm.

The transmitter section uses a multiple quantum well 1550nm DFB laser and is a class 1 laser compliant according to International Safety Standard IEC-60825. The receiver section uses an



## SFP Series

integrated InGaAs detector preamplifier (IDP) mounted in an optical header and a limiting post-amplifier IC.

### Absolute Maximum Ratings

Parameter	Symbol	Min.	Max.	Unit
Storage Temperature	T <sub>s</sub>	-40	+85	°C
Supply Voltage	V <sub>cc</sub>	-0.5	3.6	V
Operating Relative Humidity		-	95	%

\*Exceeding any one of these values may destroy the device immediately.

### Recommended Operating Conditions

Parameter	Symbol	Min.	Typical	Max.	Unit
Operating Case Temperature	T <sub>c</sub>	GD1550-SFP-LC.SXX	0	+70	°C
		GD1550-SFP-LC.SXX(WT)	-40	+85	
Power Supply Voltage	V <sub>cc</sub>	3.15	3.3	3.45	V
Power Supply Current	I <sub>cc</sub>			300	mA
Date Rate	GBE		1.25		Gbps
	FC		1.063		

### Performance Specifications – Electrical

Parameter	Symbol	Min.	Typical	Max.	Unit	Notes
<b>Transmitter</b>						
LVPECL Inputs(Differential)	V <sub>in</sub>	500		2400	mVpp	AC Coupled Inputs*(note4)
Input Impedance (Differential)	Z <sub>in</sub>	85	100	115	ohm	R <sub>in</sub> > 100 kohm @ DC
TX Disable	Disable	2		V <sub>cc</sub>	V	
	Enable	0		0.8		
TX FAULT	Fault	2		V <sub>cc</sub> +0.3	V	
	Normal	0		0.5		
<b>Receiver</b>						
LVPECL Outputs (Differential)	V <sub>out</sub>	370		2000	mVpp	AC Coupled Outputs*(note4)
Output Impedance (Differential)	Z <sub>out</sub>	85	100	115	ohms	
RX_LOS	LOS	2		V <sub>cc</sub> +0.3	V	
	Normal	0		0.8	V	
MOD_DEF ( 2:0 )	VoH	2.5			V	With Serial ID
	VoL	0		0.5	V	

**Optical and Electrical Characteristics**
**(GD1550-SFP-LC.S80, 1550nm DFB and PIN, 80km)**

Parameter	Symbol	Min.	Typical	Max.	Unit
9 $\mu$ m Core Diameter SMF	L		80		km
Data Rate			1.063/1.25		Gbps
<b>Transmitter</b>					
Center Wavelength	$\lambda_c$	1500	1550	1580	nm
Spectral Width (-20dB)	$\Delta\lambda$			1	nm
Side Mode Suppression Ratio	SMSR	30			dB
Average Output Power*(note5)	Pout	0		5	dBm
Extinction Ratio*(note6)	ER	9			dB
Rise/Fall Time(20%~80%)	tr/tf			0.26	ns
Total Jitter	TJ			0.43	UI
Output Optical Eye*(note6)	Compliant with IEEE 802.3ah-2004*(note10)				
TX Disable Assert Time	t_off			10	$\mu$ s
Pout@TX Disable Asserted	Pout			-45	dBm
<b>Receiver</b>					
Center Wavelength	$\lambda_c$	1260		1600	nm
Receiver Sensitivity*(note7)	Pmin			-24	dBm
Receiver Overload	Pmax	-3			dBm
Return Loss		12			dB
Optical Path Penalty*(note8)				1	dB
LOS De-Assert	LOSD			-25	dBm
LOS Assert	LOSA	-35			dBm
LOS Hysteresis*(note9)		0.5			dB

**(GD1550-SFP-LC.S120, 1550nm DFB and PIN, 120km)**

Parameter	Symbol	Min.	Typical	Max.	Unit
9 $\mu$ m Core Diameter SMF	L		120		km
Data Rate			1.063/1.25		Gbps
<b>Transmitter</b>					
Center Wavelength	$\lambda_c$	1500	1550	1580	nm
Spectral Width (-20dB)	$\Delta\lambda$			1	nm
Side Mode Suppression Ratio	SMSR	30			dB
Average Output Power*(note5)	Pout	0		5	dBm
Extinction Ratio*(note6)	ER	9			dB
Rise/Fall Time(20%~80%)	tr/tf			0.26	ns
Total Jitter	TJ			0.43	UI
Output Optical Eye*(note6)	Compliant with IEEE 802.3ah-2004*(note10)				
TX Disable Assert Time	t_off			10	$\mu$ s
Pout@TX Disable Asserted	Pout			-45	dBm
<b>Receiver</b>					

Center Wavelength	$\lambda_c$	1260		1600	nm
Receiver Sensitivity*(note7)	Pmin			-32	dBm
Receiver Overload	Pmax	-10			dBm
Return Loss		12			dB
Optical Path Penalty*(note8)				1	dB
LOS De-Assert	LOSD			-33	dBm
LOS Assert	LOSA	-45			dBm
LOS Hysteresis*(note9)		0.5			dB

Note4: LVPECL logic, internally AC coupled.

Note5: Output is coupled into a 9/125 $\mu$ m single-mode fiber.

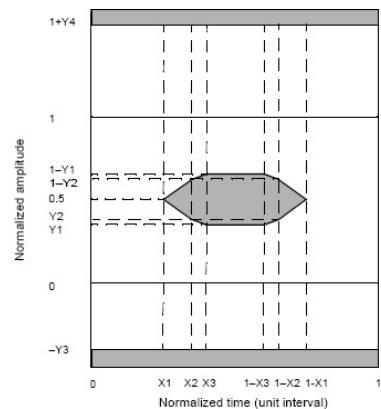
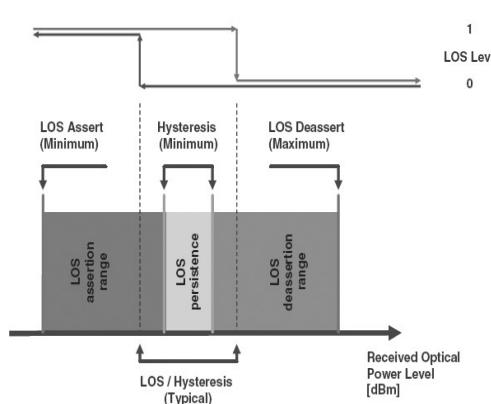
Note6: Filtered, measured with a PRBS 2<sup>7</sup>-1 test pattern @1.25Gbps

Note7: Minimum average optical power measured at BER less than 1E-12, with a 2<sup>7</sup>-1 NRZ PRBS and ER=9dB.

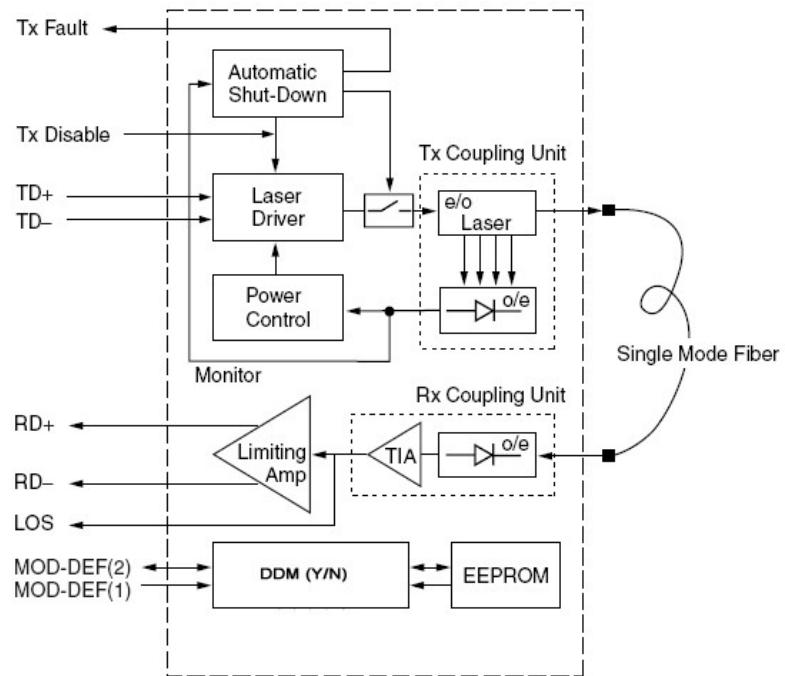
Note8: Measured with a PRBS 2<sup>7</sup>-1 test pattern @1.25Gbps, G.652 SMF, BER  $\leq 1 \times 10^{-10}$ .

Note9: LOS Hysteresis

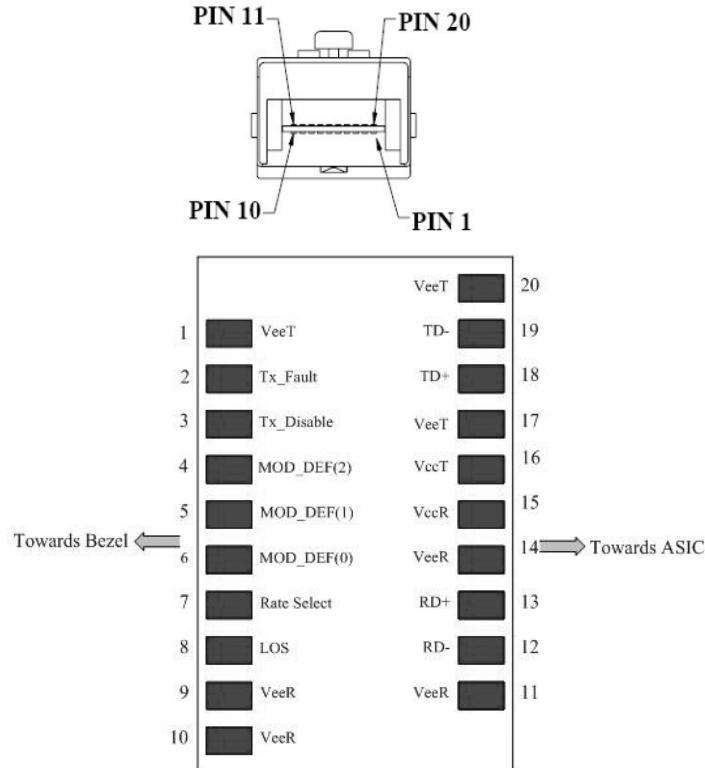
Note10: Eye Pattern Mask



## Functional Description of Transceiver



## SFP Transceiver Electrical Pad Layout



**Pin Function Definitions**

Pin Num.	Name	Function	Plug Seq.	Notes
1	VeeT	Transmitter Ground	1	5)
2	TX Fault	Transmitter Fault Indication	3	1)
3	TX Disable	Transmitter Disable	3	2) Module disables on high or open
4	MOD-DEF2	SDA	3	3) 2 wire serial ID interface.
5	MOD-DEF1	SCL	3	3) 2 wire serial ID interface.
6	MOD-DEF0	MOD_ABS	3	3) Grounded within the module.
7	Rate Select	Not Connect	3	Function not available
8	LOS	Loss of Signal	3	4)
9	VeeR	Receiver Ground	1	5)
10	VeeR	Receiver Ground	1	5)
11	VeeR	Receiver Ground	1	5)
12	RD-	Inv. Received Data Out	3	6)
13	RD+	Received Data Out	3	7)
14	VeeR	Receiver Ground	1	5)
15	VccR	Receiver Power	2	7) 3.3 ± 5%
16	VccT	Transmitter Power	2	7) 3.3 ± 5%
17	VeeT	Transmitter Ground	1	5)
18	TD+	Transmit Data In	3	8)
19	TD-	Inv. Transmit Data In	3	8)
20	VeeT	Transmitter Ground	1	5)

**Notes:**

1) TX Fault is an open collector/drain output, which should be pulled up with a 4.7K – 10KΩ resistor on the host board. Pull up voltage between 2.0V and VccT/R+0.3V. When high, output indicates a laser fault of some kinds. Low indicates normal operation. In the low state, the output will be pulled to < 0.8V.

2) TX disable is an input that is used to shut down the transmitter optical output. It is pulled up within the module with a 4.7K – 10 KΩ resistor. Its states are:

Low (0 – 0.8V): Transmitter on

(>0.8, < 2.0V): Undefined

High (2.0 – 3.465V): Transmitter Disabled

Open: Transmitter Disabled

3) Mod-Def 0,1,2. These are the module definition pins. They should be pulled up with a 4.7K – 10K resistor on the host board. The pull-up voltage shall be VccT or VccR .

Mod-Def 0 is grounded by the module to indicate that the module is present

Mod-Def 1 is the clock line of two wire serial interface for serial ID

Mod-Def 2 is the data line of two wire serial interface for serial ID

4) LOS (Loss of Signal) is an open collector/drain output, which should be pulled up with a 4.7K – 10KΩ resistor. Pull up voltage between 2.0V and VccT/R+0.3V. When high, this output indicates the received optical power is below the worst-case receiver sensitivity (as defined by the standard in use). Low indicates normal operation. In the low state, the output will be pulled to < 0.8V.

5) VeeR and VeeT may be internally connected within the SFP module.

6) RD-+: These are the differential receiver outputs. They are AC coupled 100Ω differential lines which should be terminated with 100Ω (differential) at the user SERDES. The AC coupling is done inside the module and is thus not required on the host board.

7) VccR and VccT are the receiver and transmitter power supplies. They are defined as 3.3V ±5% at the SFP connector pin. Maximum supply current is 300mA. Recommended host board power supply filtering is shown below. Inductors with DC resistance of less than 1 ohm should be used in order to maintain the required voltage at the SFP input pin with 3.3V supply voltage. When the recommended supply-filtering network is used, hot plugging of the SFP transceiver module will result in an inrush current of no more than 30mA greater than the steady state value. VccR and VccT may be internally connected within the SFP transceiver module.

8) TD-+: These are the differential transmitter inputs. They are AC-coupled, differential lines with 100Ω differential termination inside the module. The AC coupling is done inside the module.

### EEPROM

The serial interface uses the 2-wire serial I<sup>2</sup>C protocol. When the serial protocol is activated, the host generates the serial clock signal (SCL). The positive edge clocks data into those segments of the EEPROM that are not write protected within the SFP transceiver. The negative edge clocks data from the SFP transceiver. The serial data signal (SDA) is bi-directional for serial data transfer. The host uses SDA in conjunction with SCL to mark the start and end of serial protocol activation. The memories are organized as a series of 8-bit data words that can be addressed individually or sequentially.

The Module provides diagnostic information about the present operating conditions. The transceiver generates this diagnostic data by digitization of internal analog signals. Calibration and alarm/warning threshold data is written during device manufacture. Received power monitoring, transmitted power monitoring, bias current monitoring, supply voltage monitoring and temperature monitoring all are implemented. If the module is defined as external calibrated, the diagnostic data are raw A/D values and must be converted to real world units using calibration constants stored in EEPROM locations 56 – 95 at wire serial bus address A2h. The digital diagnostic memory map specific data field define as following .For detail EEPROM information, please refer to the related document of SFF 8472 Rev 9.3.

2 wire address 1010000X (A0h)		2 wire address 1010001X (A2h)	
0	Serial ID Defined by SFP MSA (96 bytes)	0	Alarm and Warning Thresholds (56 bytes)
95	Vendor Specific (32 bytes)	55	Cal Constants (40 bytes)
127	Reserved (128 bytes)	95	Real Time Diagnostic Interface (24 bytes)
255		119	Vendor Specific (8 bytes)
		127	User Writable EEPROM (120 bytes)
		247	Vendor Specific (8 bytes)
		255	

### EEPROM Serial ID Memory Contents

Accessing Serial ID Memory uses the 2 wire address 1010000X(A0h). Memory Contents of Serial ID are shown in Table 1, Table 2.

**Table 1 Serial ID Memory Contents**

----- GD1550-SFP-LC.S40,60,80Series

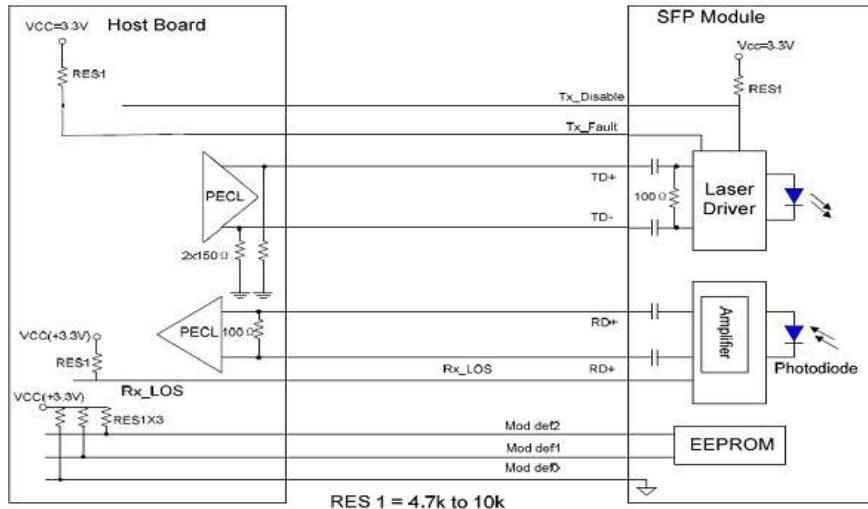
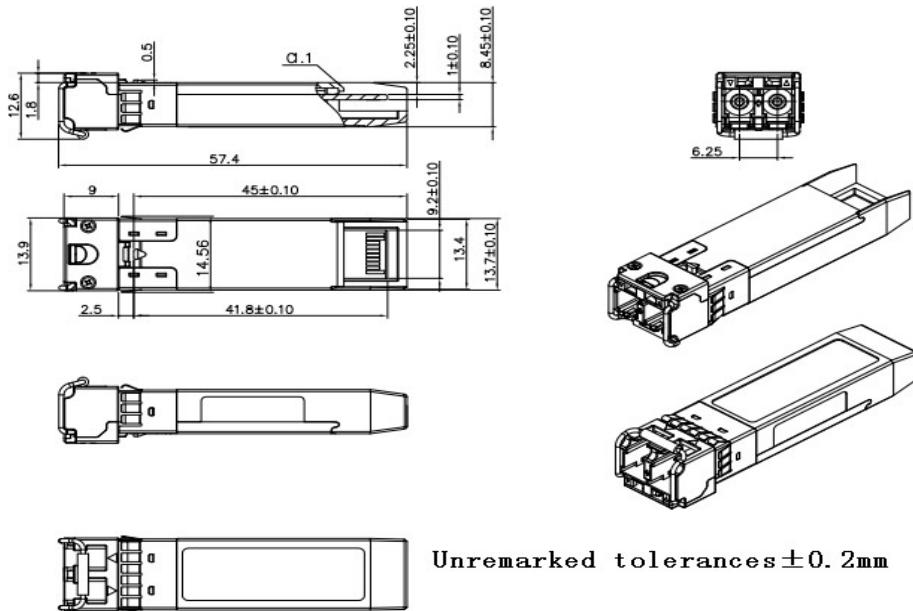
Add.	Size (Bytes)	Name of Field	Hex	Description
<b>BASE ID FIELDS</b>				
0	1	Identifier	03	SFP
1	1	Ext. Identifier	04	SFP function is defined by serial ID only
2	1	Connector	07	LC Connector
3-10	8	Transceiver	00 00 00 02 10 10 01 01	Transmitter Code
11	1	Encoding	01	8B10B
12	1	BR, Nominal	0D	1.25Gbps
13	1	Reserved	00	
14	1	Length (9µm) km	28/3C/50/64	Transceiver Transmit Distance
15	1	Length(9µm)100m	FF/FF/FF/FF	
16	1	Length (50µm) 10m	00	
17	1	Length(62.5µm)10m	00	
18	1	Length (Copper)	00	Not Compliant
19	1	Reserved	00	
20-35	16	Vendor name	XX XX XX XX XX XX XX XX XX <sup>(note11)</sup> 20 20 20 20 20 20 20 20	Vendor name (ASCII)
36	1	Reserved	00	



## SFP Series

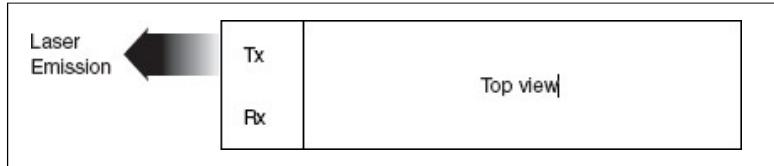
37-39	3	Vendor OUI	00 00 00	
40-55	16	Vendor PN	XX XX XX XX XX XX XX XX XX XX XX XX XX XX XX XX	Transceiver part number
56-59	4	Vendor Rev	XX XX XX XX <sup>(note11)</sup>	
60-61	2	Wavelength	06 0E	1550nm
62	1	Reserved	00	
63	1	CC_BASE	Checksum	Check code for Base ID Fields
<b>EXTENDED ID FIELDS</b>				
64-65	2	Options	00 1A	TX_DISABLE, TX_FAULT and Loss of Signal Implemented.
66	1	BR, max	00	
67	1	BR,min	00	
68-83	16	Vendor SN	XX XX XX XX XX XX XX XX 20 20 20 20 20 20 20 20 <sup>(note11)</sup>	Serial Number of Transceiver (ASCII). For example "B000822".
84-91	8	Date Code	XX XX XX XX XX XX XX XX <sup>(note11)</sup>	Manufactory Date Code. For example "080405".
92	1	Diagnostic Monitoring Type	68	DD Implemented; Internally calibrated; Average Power
93	1	Enhanced Options	XX <sup>(note11)</sup>	Optional Flags
94	1	SFF_8472 Compliance	XX <sup>(note11)</sup>	01 for Rev9.3 SFF-8472.
95	1	CC_EXT	Checksum	Checksum for Extended ID Field.
<b>VENDOR SPECIFIC ID FIELDS</b>				
96-127	32	Vendor Specific	Read Only	Depends on Customer Information
128-255	128	Reserved	Read Only	

Note11: The "XX" byte should be filled in according to practical case. For more information, please refer to the related document of SFP Multi-Source Agreement (MSA).

**Recommended Circuit Schematic**

**Mechanical Specifications**


\*This 2D drawing only for reference, please check with Data Controls Inc. before ordering.

## Laser Emission



## Obtaining Document

You can visit our website:

<https://www.dci.jp/>

Or contact Data Controls Inc. Listed at the end of the documentation to get the latest documents.

## Revision History

Revision	Revision History	Release Date
V3.a	Released.	Feb. 10, 2010
V3.b	Update PN.	Dec. 17, 2010
V3.c	Update LVPECL input and output.	May 4, 2011
V4.a	Update circuit schematic.	July 11, 2011
V4.b	Integrate 5 products.	Sep 22, 2011
V4.c	Add 160km products.	Oct 10, 2011
V4.d	Update pin definition notes	Jan 23, 2013
V4.e	Update output power of ES-1512-C-D.	Apr 27, 2013
V4.f	Update LOSD&LOSA	August 13, 2013
V4.g	Update ordering information, regulatory compliance and LOSA&LOSD.	Aug. 1, 2014
V4.h	Clear and definite internally calibrated in A0h.	Oct 21, 2014
V4.i	Update Mechanical Specifications and some mistakes	Jul 7, 2015

V4.j	Update the regulatory compliance and correct some slip of the pen.	Jan 26,2016
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