

HDLR4-CFP-LC.S10

Single-Mode 100GBASE-LR4 CFP Transceiver
RoHS6 Compliant



Features

- ◆ Supports 103Gbps and 112Gbps aggregate bit rates
- ◆ Single 3.3V Power Supply and Power dissipation < 16W
- ◆ Up to 10km transmission on SMF
- ◆ Hot-Pluggable CFP Footprint Duplex LC Connector Interface
- ◆ Class 1 FDA and IEC60825-1 Laser Safety Compliant
- ◆ RoHS6 Compliant
- ◆ Operating Case Temperature Extended: -10°C~+75°C
- ◆ Compliant with CFP MSA Specification
- ◆ MDIO interface with integrated Digital Diagnostic Monitoring
- ◆ CAUI electrical interface

Applications

- ◆ 100GBASE-LR4 Ethernet
- ◆ Data Center

Ordering Information

Part No.	Data Rate	Fiber	Distance ^{*(Note2)}	Interface	Temp.	DDMI
HDLR4-CFP-LC.S10 ^{*(Note1)}	103Gbps ^{*(Note3)}	SMF	10km	LC	Extended	Yes

Note1: Standard version

Note2: 10km with 9/125µm SMF

Note3: can change to OTL4.4 rate through MDIO, but do not guarantee OTL4.4 specification.

Regulatory Compliance*

Product Certificate	Certificate Number	Applicable Standard
TUV	R50135086	EN 60950-1:2006+A11+A1+A12
		EN 60825-1:2007
		EN 60825-2:2004+A1+A2
UL	E317337	UL 60950-1
		CSA C22.2 No. 60950-1-07
EMC CE	AE 50285865 0001	EN 55022:2010
		EN 55024:2010
CB	JPTUV-049251	IEC 60825-1
		IEC 60950-1
FCC	WTF14F0514437E	47 CFR PART 15 OCT., 2013
FDA	1331340-000	CDRH 1040.10
ROHS	RHS01G006464	2011/65/EU

*The above certificate number updated to June 2014, because some certificate will be updated every year, such as FCC, FDA and ROHS. For the latest certification information, please check with Data Controls Inc.

Absolute Maximum Ratings*^{Note4}

Parameter	Symbol	Min.	Max.	Unit
Storage Temperature	T _s	-40	+85	°C
Supply Voltage	V _{cc}	-0.5	3.6	V
Operating Relative Humidity	RH	5	85	%

Note4: Exceeding any one of these values may destroy the device immediately.

Recommended Operating Conditions

Parameter	Symbol	Min.	Typical	Max.	Unit
Operating Case Temperature	T _c HDLR4-CFP-LC.S10	-10		+75	°C
Power Supply Voltage	V _{cc}	3.2	3.3	3.4	V
Power Supply Current	I _{cc}		4000		mA

Performance Specifications - Electrical

Parameter	Symbol	Min.	Typ.	Max	Unit	Notes
Transmitter						
Input Amplitude (Differential)	V _{in}			1050	mV _{pp}	AC coupled inputs* ^(Note6)
Input Impedance (Differential)	Z _{in}	80	100	120	ohms	R _{in} > 100 kohms @ DC
Receiver						
Output Amplitude (Differential)	V _{out}	360		770	mV _{pp}	AC coupled outputs* ^(Note6)
Output Impedance	Z _{out}	80	100	120	ohms	

(Differential)						
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1.2V MDIO Interface Specifications

Parameter	Symbol	Min.	Typ.	Max	Unit	Notes
Input Voltage	V _{IH}	0.84		1.5	V	
	V _{IL}	-0.3		0.36	V	
Input Leak current	I _{IN}	-100		100	uA	
Output Voltage	V _{OH}	1.0		1.5	V	
	V _{OL}	-0.3		0.2	V	
Input Capacitance	C _i			10	pF	
Input MDC Clock	f _{MDC}	0.1		4	MHz	
MDC Clock Period	T _{MDC}	250		10000	ns	
MDIO Hold Time	T _{hold}	10			ns	
MDIO SetupTime	T _{setup}	10			ns	
GLB_ALM	T _{glb_alm_ass}			150	ms	
	T _{glb_alm_dea}			150	ms	

Optical and Electrical Characteristics

Parameter	Symbol	Min.	Typical	Max.	Unit
Transmitter					
Signaling Speed per Lane	BR _{AVE}		25.78		Gbps
Lane_0 Center Wavelength	λ _{C0}	1294.53	1295.56	1296.59	nm
Lane_1 Center Wavelength	λ _{C1}	1299.02	1300.05	1301.09	nm
Lane_2 Center Wavelength	λ _{C2}	1303.54	1304.58	1305.63	nm
Lane_3 Center Wavelength	λ _{C3}	1308.09	1309.14	1310.19	nm
Total Average Output Power*(Note5)	P _o			10.5	dBm
Average Launch Power per Lane	P _{each}	-4.3		4.5	dBm
Side Mode Suppression Ratio	SMSR	30			dB
Optical Return Loss Tolerance				20	dB
Extinction Ratio*(Note10)	ER	4			dB
Transmitter eye mask definition {X1, X2, X3, Y1, Y2, Y3}*(Note8)			IEEE 802.3ba-2010 Compliant		
TX Disable Assert Time	t _{off}			100	us
Receiver					
Signaling Speed per Lane	BR _{AVE}		25.78		Gbps
Lane_0 Center Wavelength	λ _{C0}	1294.53	1295.56	1296.59	nm
Lane_1 Center Wavelength	λ _{C1}	1299.02	1300.05	1301.09	nm
Lane_2 Center Wavelength	λ _{C2}	1303.54	1304.58	1305.63	nm
Lane_3 Center Wavelength	λ _{C3}	1308.09	1309.14	1310.19	nm
Average Receive Power per Lane*(Note7)	R _{pow}	-10.6		4.5	dBm
Receiver Overload per Lane	P _{max}	4.5			dBm
Optical Return Loss	ORL			-26	dB
LOS Assert	LOSA	-22			dBm

LOS De-Assert	LOSD			-12	dBm
LOS Hysteresis*(Note9)		0.5			dB

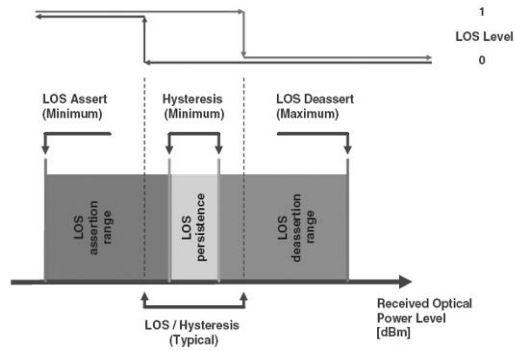
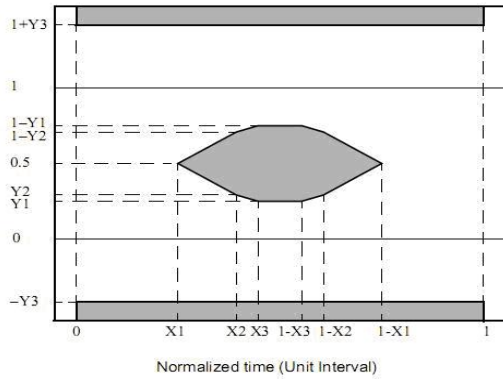
Note5: Output is coupled into a 9/125µm single-mode fiber.

Note6: High speed I/O, internally AC coupled.

Note7: Average optical power measured at BER less than 1E-12, with a 2³¹-1 PRBS.

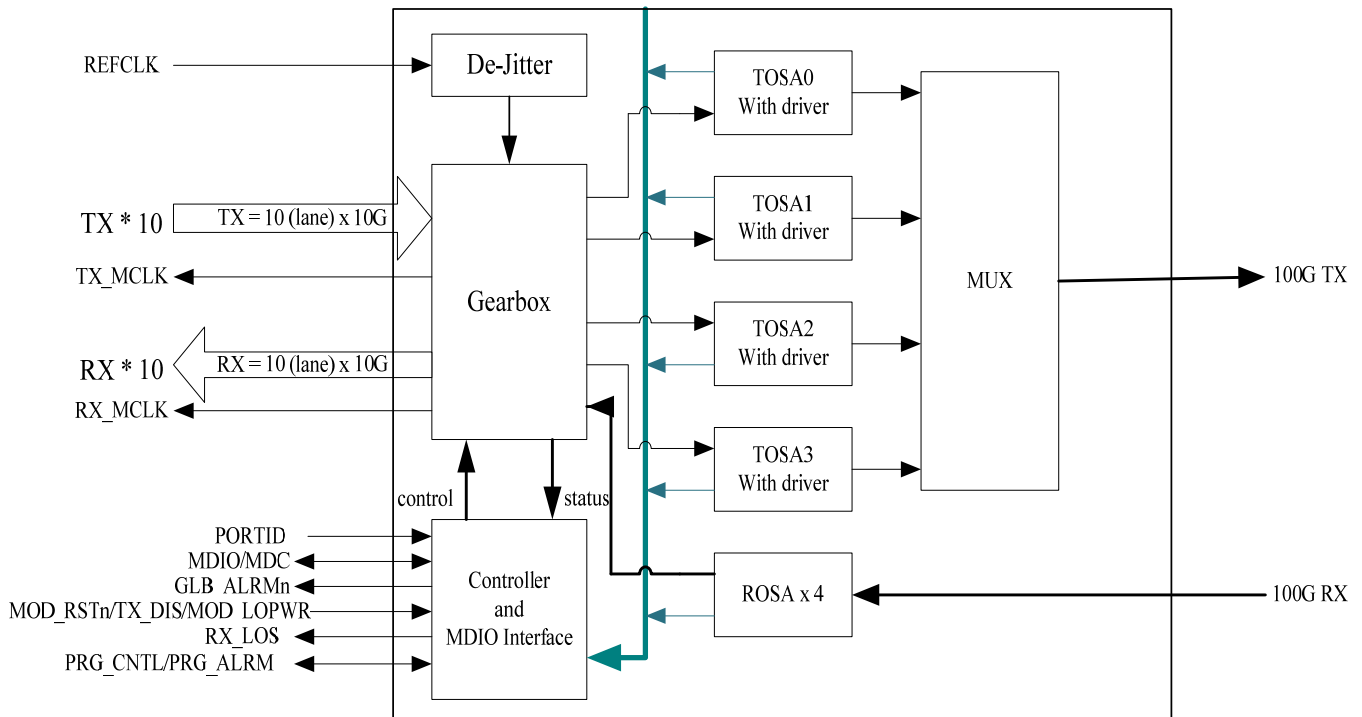
Note8: Eye Pattern Mask

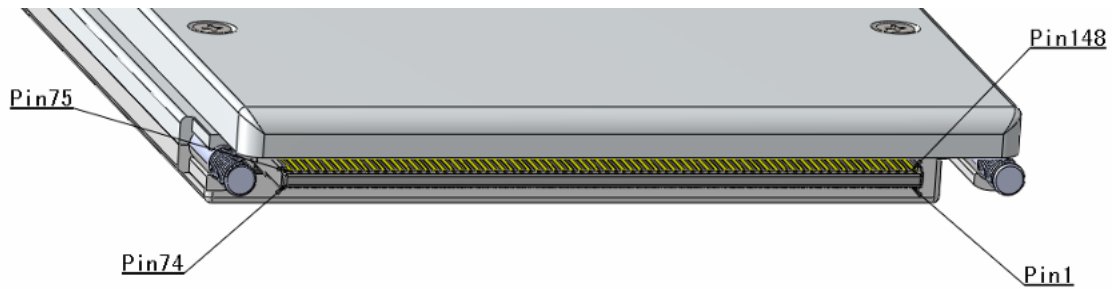
Note9: LOS Hysteresis



Note10: Filtered, measured with a PRBS 2³¹-1 test pattern @25.78Gbps

Functional Description of Transceiver



CFP Transceiver Electrical Pad Layout

Pin Function Definitions

CFP			
Top Row			
148	GND	111	GND
147	REFCLKn	110	N.C.
146	REFCLKp	109	N.C.
145	GND	108	GND
144	N.C.	107	RX9n
143	N.C.	106	RX9p
142	GND	105	GND
141	TX9n	104	RX8n
140	TX9p	103	RX8p
139	GND	102	GND
138	TX8n	101	RX7n
137	TX8p	100	RX7p
136	GND	99	GND
135	TX7n	98	RX6n
134	TX7p	97	RX6p
133	GND	96	GND
132	TX6n	95	RX5n
131	TX6p	94	RX5p
130	GND	93	GND
129	TX5n	92	RX4n
128	TX5p	91	RX4p
127	GND	90	GND
126	TX4n	89	RX3n
125	TX4p	88	RX3p
124	GND	87	GND
123	TX3n	86	RX2n
122	TX3p	85	RX2p
121	GND	84	GND
120	TX2n	83	RX1n
119	TX2p	82	RX1p

CFP			
Bottom Row			
1	3.3V_GND	38	MOD_ABS
2	3.3V_GND	39	MOD_RSTn
3	3.3V_GND	40	RX_LOS
4	3.3V_GND	41	GLB_ALRMn
5	3.3V_GND	42	PRTADR4
6	3.3V	43	PRTADR3
7	3.3V	44	PRTADR2
8	3.3V	45	PRTADR1
9	3.3V	46	PRTADR0
10	3.3V	47	MDIO
11	3.3V	48	MDC
12	3.3V	49	GND
13	3.3V	50	VND_IO_F
14	3.3V	51	VND_IO_G
15	3.3V	52	GND
16	3.3V_GND	53	VND_IO_H
17	3.3V_GND	54	VND_IO_J
18	3.3V_GND	55	3.3V_GND
19	3.3V_GND	56	3.3V_GND
20	3.3V_GND	57	3.3V_GND
21	VND_IO_A	58	3.3V_GND
22	VND_IO_B	59	3.3V_GND
23	GND	60	3.3V
24	(TX_MCLKn)	61	3.3V
25	(TX_MCLKp)	62	3.3V
26	GND	63	3.3V
27	VND_IO_C	64	3.3V
28	VND_IO_D	65	3.3V
29	VND_IO_E	66	3.3V
30	PRG_CNTL1	67	3.3V

118	GND	81	GND
117	TX1n	80	RX0n
116	TX1p	79	RX0p
115	GND	78	GND
114	TX0n	77	(RX_MCLKn)
113	TX0p	76	(RX_MCLKp)
112	GND	75	GND

31	PRG_CNTL2	68	3.3V
32	PRG_CNTL3	69	3.3V
33	PRG_ALARM1	70	3.3V_GND
34	PRG_ALARM2	71	3.3V_GND
35	PRG_ALARM3	72	3.3V_GND
36	TX_DIS	73	3.3V_GND
37	MOD_LOPWR	74	3.3V_GND

Bottom Row Pin Descriptions

Pin Num.	Name	Function	Notes
1	3.3V_GND	Ground	3.3V Module Supply Ground, Internally connected to Signal Ground
2	3.3V_GND		
3	3.3V_GND		
4	3.3V_GND		
5	3.3V_GND		
6	3.3V	3.3V Module Supply Voltage	3.3V ± 3%
7	3.3V		
8	3.3V		
9	3.3V		
10	3.3V		
11	3.3V		
12	3.3V		
13	3.3V		
14	3.3V		
15	3.3V		
16	3.3V_GND	Ground	3.3V Module Supply Ground, Internally connected to Signal Ground
17	3.3V_GND		
18	3.3V_GND		
19	3.3V_GND		
20	3.3V_GND		
21	VND_IO_A	I/O	Module Vendor I/O, NC
22	VND_IO_B	I/O	Module Vendor I/O, NC
23	GND	Ground	Signal Ground
24	TX_MCLKn	Tx Monitor Clock O	Tx Monitor Clock Output
25	TX_MCLKp		
26	GND	Ground	Signal Ground
27	VND_IO_C	I/O	Module Vendor I/O, Must not connect at host board
28	VND_IO_D	I/O	Module Vendor I/O, Must not connect at host board
29	VND_IO_E	I/O	Module Vendor I/O, Must not connect at host board
30	PRG_CNTL1	I	LVCOMS

31	PRG_CNTL2	I	LVCOMS
32	PRG_CNTL3	I	LVCOMS
33	PRG_ALARM1	O	LVCOMS
34	PRG_ALARM2	O	LVCOMS
35	PRG_ALARM3	O	LVCOMS
36	TX_DIS	Transmitter Disable I	"1" or NC = transmitter disabled, "0" = transmitter enabled
37	MOD_LOPW R	Module Low Power Mode I	"1" or NC: module in low power (safe) mode, "0": power-on enabled
38	MOD_ABS	Module Absent O	"1" or NC: module absent, "0": module present
39	MOD_RSTn	Module Reset I	"0" resets the module, "1" or NC = module enabled
40	RX_LOS	Loss of Signal O	"1": low optical signal, "0": normal condition
41	GLB_ALRMn	Global Alarm O	"0": alarm condition in any MDIO Alarm register, "1": no alarm condition
42	PRTADR4	1.2V CMOS I	MDIO Physical Port address bit4
43	PRTADR3	1.2V CMOS I	MDIO Physical Port address bit3
44	PRTADR2	1.2V CMOS I	MDIO Physical Port address bit2
45	PRTADR1	1.2V CMOS I	MDIO Physical Port address bit1
46	PRTADR0	1.2V CMOS I	MDIO Physical Port address bit0
47	MDIO	1.2V CMOS I/O	Management Data I/O bi-directional data
48	MDC	1.2V CMOS I	Management Data Clock
49	GND	Ground	Signal Ground
50	VND_IO_F	I/O	Module Vendor I/O, Not Connected Internally
51	VND_IO_G	I/O	Module Vendor I/O, Not Connected Internally
52	GND	Ground	Signal Ground
53	VND_IO_H	I/O	Module Vendor I/O, Not Connected Internally
54	VND_IO_J	I/O	Module Vendor I/O, Not Connected Internally

55	3.3V_GND	Ground	3.3V Module Supply Ground, Internally connected to Signal Ground
56	3.3V_GND		
57	3.3V_GND		
58	3.3V_GND		
59	3.3V_GND		
60	3.3V	3.3V Module Supply Voltage	3.3V ± 3%
61	3.3V		
62	3.3V		
63	3.3V		
64	3.3V		
65	3.3V		
66	3.3V		
67	3.3V		
68	3.3V		
69	3.3V		
70	3.3V_GND	Ground	3.3V Module Supply Ground, Internally connected to Signal Ground
71	3.3V_GND		
72	3.3V_GND		
73	3.3V_GND		
74	3.3V_GND		

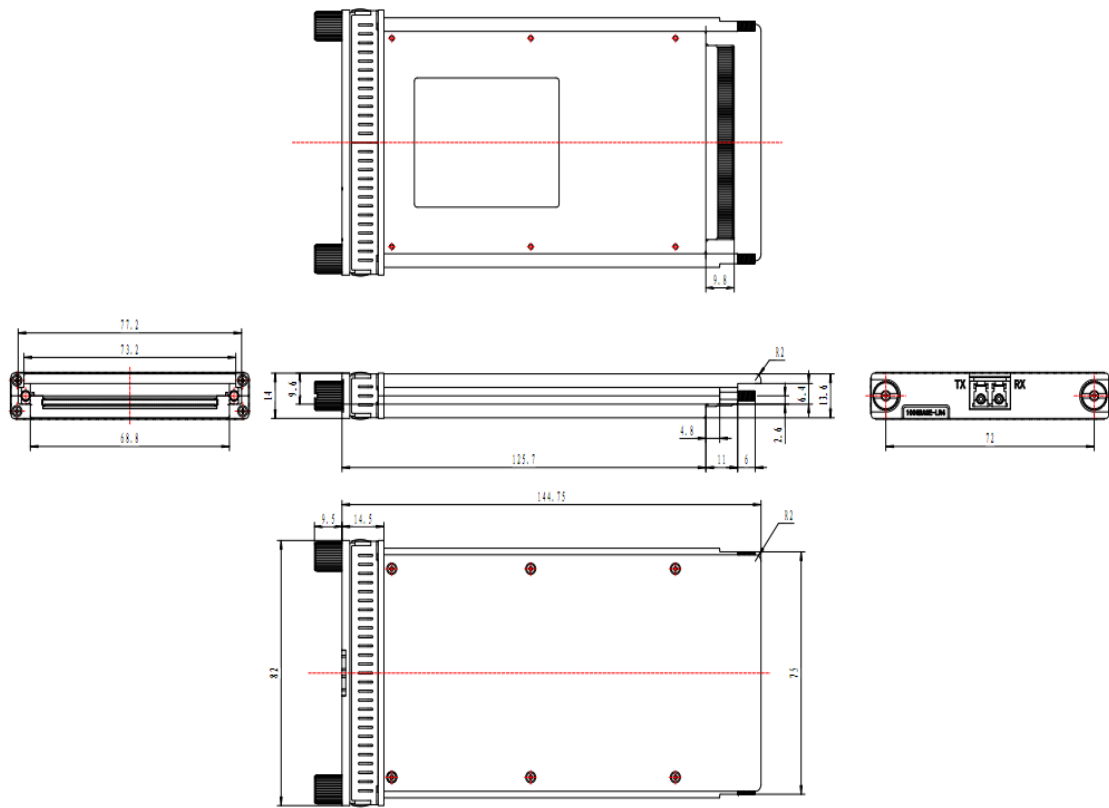
Top Row Pin Descriptions

Pin Num.	Name	Function	Notes
148	GND	Ground	Signal Ground
147	REFCLKn	Reference Clock I	Reference Clock Input
146	REFCLKp		
145	GND	Ground	Signal Ground
144	N.C.		Not Connected Internally
143	N.C.		Not Connected Internally
142	GND	Ground	Signal Ground
141	TX9n	Lane 9 Tx Input I	CML Input
140	TX9p		
139	GND	Ground	Signal Ground
138	TX8n	Lane 8 Tx Input I	CML Input
137	TX8p		
136	GND	Ground	Signal Ground
135	TX7n	Lane 7 Tx Input I	CML Input
134	TX7p		
133	GND	Ground	Signal Ground
132	TX6n	Lane 6 Tx Input I	CML Input
131	TX6p		
130	GND	Ground	Signal Ground
129	TX5n	Lane 5 Tx Input	CML Input

128	TX5p	I	
127	GND	Ground	Signal Ground
126	TX4n	Lane 4 Tx Input	CML Input
125	TX4p	I	
124	GND	Ground	Signal Ground
123	TX3n	Lane 3 Tx Input	CML Input
122	TX3p	I	
121	GND	Ground	Signal Ground
120	TX2n	Lane 2 Tx Input	CML Input
119	TX2p	I	
118	GND	Ground	Signal Ground
117	TX1n	Lane 1 Tx Input	CML Input
116	TX1p	I	
115	GND	Ground	Signal Ground
114	TX0n	Lane 0 Tx Input	CML Input
113	TX0p	I	
112	GND	Ground	Signal Ground
111	GND	Ground	Signal Ground
110	N.C.		Not Connected Internally
109	N.C.		Not Connected Internally
108	GND	Ground	Signal Ground
107	RX9n	Lane 9 Rx Output	CML Output
106	RX9p	O	
105	GND	Ground	Signal Ground
104	RX8n	Lane 8 Rx Output	CML Output
103	RX8p	O	
102	GND	Ground	Signal Ground
101	RX7n	Lane 7 Rx Output	CML Output
100	RX7p	O	
99	GND	Ground	Signal Ground
98	RX6n	Lane 6 Rx Output	CML Output
97	RX6p	O	
96	GND	Ground	Signal Ground
95	RX5n	Lane 5 Rx Output	CML Output
94	RX5p	O	
93	GND	Ground	Signal Ground
92	RX4n	Lane 4 Rx Output	CML Output
91	RX4p	O	
90	GND	Ground	Signal Ground
89	RX3n	Lane 3 Rx Output	CML Output
88	RX3p	O	
87	GND	Ground	Signal Ground
86	RX2n	Lane 2 Rx Output	CML Output
85	RX2p	O	

84	GND	Ground	Signal Ground
83	RX1n	Lane 1 Rx Output	CML Output
82	RX1p	O	
81	GND	Ground	Signal Ground
80	RX0n	Lane 0 Rx Output	CML Output
79	RX0p	O	
78	GND	Ground	Signal Ground
77	RX_MCLKn	Rx Monitor Clock	Rx Monitor Clock Output
76	RX_MCLKp	O	
75	GND	Ground	Signal Ground

Mechanical Specifications



Laser Emission

