

DGxx-SFP-LC.S80 Series

SFP Single-Mode for DWDM Application

Duplex SFP Transceiver

Digital Diagnostic Function RoHS6 Compliant



Features

- ◆ Operating Data Rate up to 1.25Gbps
- ◆ Available in all C-Band Wavelengths on the 100GHz DWDM ITU Grid
- ◆ Single 3.3V Power Supply and TTL Logic Interface
- ◆ Hot-Pluggable SFP Footprint Duplex LC Connector Interface
- ◆ Compliant with Class 1 FDA and IEC60825-1 Laser Safety
- ◆ Compliant with SFP MSA
- ◆ Compliant with SFF-8472
- ◆ Operating Case Temperature: Standard: 0°C to 70°C Industrial: -40°C to 85°C

Applications

- ◆ Amplified DWDM networks
- ◆ Ring topologies with fixed and reconfigurable OADMs
- ◆ Fast Ethernet, Giga Ethernet
- ◆ Fiber Channel
- ◆ CPRI rate: 1.229 Gb/s

Ordering Information

Part No.	Data Rate	Laser	Power budget* (note2)	Interface	Temperature
DGxx-SFP-LC.S80 ^(note1)	1.25Gbps	DWDM EML	24dB	LC	Standard
DGxx-SFP-LC.S80(WT)	1.25Gbps	DWDM EML	24dB	LC	Industrial

*The product image only for reference purpose.

Note1: XX refers to DWDM Wavelength range as ITU-T specified, please refer the following table for detailed center wavelength information.

Note2: The power budget which is guaranteed.

XX- Channel refers to the following table:

Channel (XX)	Frequency (THz)	Center Wavelength (nm)
15	191.5	1565.50
16	191.6	1564.68
17	191.7	1563.86
18	191.8	1563.05
19	191.9	1562.23
20	192.0	1561.42
21	192.1	1560.61
22	192.2	1559.79
23	192.3	1558.98
24	192.4	1558.17
25	192.5	1557.36
26	192.6	1556.55
27	192.7	1555.75
28	192.8	1554.94
29	192.9	1554.13
30	193.0	1553.33
31	193.1	1552.52
32	193.2	1551.72
33	193.3	1550.92
34	193.4	1550.12
35	193.5	1549.32
36	193.6	1548.51
37	193.7	1547.72
38	193.8	1546.92
39	193.9	1546.12
40	194.0	1545.32
41	194.1	1544.53
42	194.2	1543.73
43	194.3	1542.94
44	194.4	1542.14
45	194.5	1541.35
46	194.6	1540.56
47	194.7	1539.77
48	194.8	1538.98
49	194.9	1538.19
50	195.0	1537.40
51	195.1	1536.61
52	195.2	1535.82
53	195.3	1535.04
54	195.4	1534.25
55	195.5	1533.47
56	195.6	1532.68
57	195.7	1531.90
58	195.8	1531.12
59	195.9	1530.33
60	196.0	1529.55
61	196.1	1528.77

Regulatory Compliance*^{Note3}

Product Certificate	Certificate Number	Applicable Standard
TUV	R50135086	EN 60950-1:2006+A11+A1+A12+A2
		EN 60825-1:2014
		EN 60825-2:2004+A1+A2
UL	E317337	UL 60950-1
		CSA C22.2 No. 60950-1-07
EMC CE	AE 50285865 0001	EN 55022:2010
		EN 55024:2010
FCC	WTF14F0514417E	47 CFR PART 15 OCT., 2013
FDA	/	CDRH 1040.10
ROHS	/	2011/65/EU

Note3: The above certificate number updated to June 2014, because some certificate will be updated every year, such as FDA and ROHS. For the latest certification information, please check with Data Controls Inc.

Product Description

The DGxx-SFP-LC.S80 series single mode transceiver is small form factor pluggable module for duplex optical data communications. This module is designed for single mode fiber and operates at a nominal DWDM wavelength from 1528.77nm to 1565.50nm as specified by the ITU-T. It is designed to deploy in the DWDM networking equipment in metropolitan access and core networks.

It is with the SFP 20-pin connector to allow hot plug capability. The transmitter section uses a DWDM multiple quantum well EML laser and is a class 1 laser compliant according to International Safety Standard IEC-60825.

The DGxx-SFP-LC.S80 Series are designed to be compliant with SFF-8472 Multi-Source Agreement (MSA).

Absolute Maximum Ratings

Parameter	Symbol	Min.	Max.	Unit
Storage Temperature	T _S	-40	+85	°C
Supply Voltage	V _{CC}	-0.5	3.6	V
Operating Relative Humidity		-	95	%

*Exceeding any one of these values may destroy the device permanently.

Recommended Operating Conditions

Parameter	Symbol	Min.	Typical	Max.	Unit
Operating Case Temperature	T _c DGxx-SFP-LC.S80	0	-	+70	°C
	DGxx-SFP-LC.S80(WT)	-40	-	+85	
Power Supply Voltage	V _{CC}	3.15	3.3	3.45	V
Power Supply Current	I _{CC}	-	-	450	mA
Power Supply Current	I _{CC}	-	-	540*	mA
Date Rate				1.25G	bps

*540mA only for Industrial operating case temperature

Performance Specifications - Electrical

Parameter	Symbol	Min.	Typ.	Max	Unit	Notes
Transmitter						
LVPECL Inputs(Differential)	V _{in}	400		2000	mVpp	AC coupled inputs*(note4)
Input Impedance (Differential)	Z _{in}	85	100	115	ohm	R _{in} > 100 kohm @ DC
TX_Dis	Disable	2		V _{CC}	V	
	Enable	0		0.8		
TX_FAULT	Fault	2		V _{CC}	V	
	Normal	0		0.8		
Receiver						
LVPECL Outputs (Differential)	V _{out}	370		2000	mVpp	AC coupled outputs*(note4)
Output Impedance (Differential)	Z _{out}	85	100	115	ohm	
RX_LOS	LOS	2		V _{CC}	V	
	Normal	0		0.8	V	
MOD_DEF (0:2)	VoH	2.5			V	
	VoL	0		0.8	V	

Performance Specifications – Optical

(DWDM EML and PIN/TIA, 24dB Power Budget at Least)

Parameter	Symbol	Min.	Typical	Max.	Unit
Data Rate			1.25G		bps
Transmitter					
Center Wavelength	λ	1528		1566	nm
Spectral Width (-20dB)	$\Delta\lambda$			0.3	nm
Side Mode Suppression Ratio	SMSR	30			dB
Channel Spacing	Δf		100		GHz
Deviation From Central Frequency@EOL		-12		12	GHz
Average Output Power*(note5)	P _{out}	0		5	dBm
Average Launch Power (Tx: OFF)	P _{off}			-45	dBm
Extinction Ratio*(note6)	ER	8.2			dB
Rise/Fall Time(20%~80%)	tr/tf			260	ps
Output Optical Eye*(note6)	Compatible with IEEE 802.3*(note8)				
TX_Disable Assert Time	t _{off}			10	us
P _{out} @TX Disable Asserted	P _{out}			-45	dBm
Optical Signal Noise Ratio @ 0.1nm	OSNR		40		dB
Relative Intensity Noise	RIN			-135	dB/Hz
Dispersion Tolerance	DT		1760		ps/nm
Receiver					
Center Wavelength	λ	1528		1566	nm
Receiver Sensitivity*(note7)	P _{min}			-24	dBm
Receiver Overload	P _{max}	-3			dBm
LOS De-Assert	LOSD			-25	dBm
LOS Assert	LOSA	-35			dBm
LOS Hysteresis*(note9)		0.5			dB

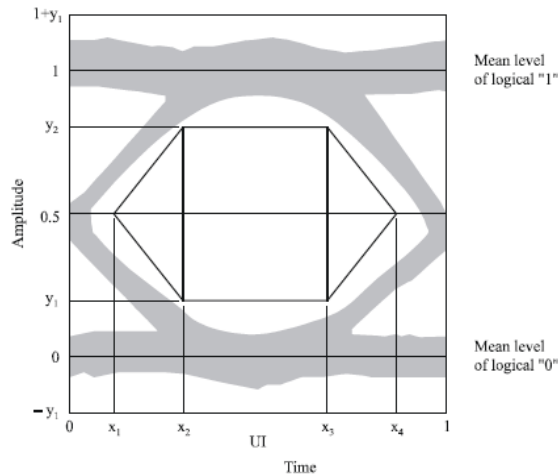
Note4: LVPECL logic, internally AC coupled.

 Note5: Output is coupled into a 9/125 μ m single-mode fiber.

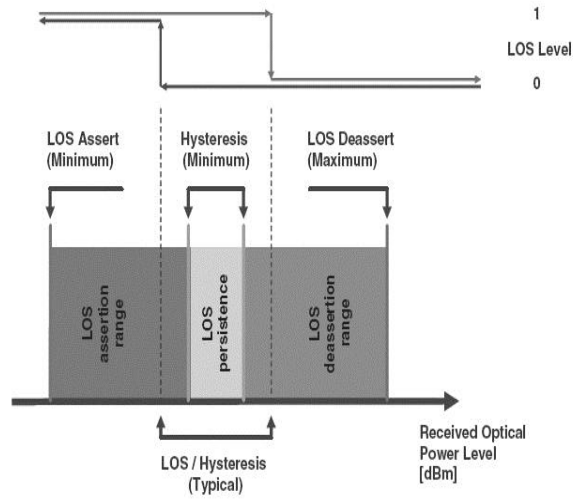
 Note6: Filtered, measured with a PRBS 2⁷-1 test pattern @1.25Gbps

 Note7: Minimum average optical power measured at BER less than 1E-12, with a 2⁷-1 PRBS and ER=9dB.

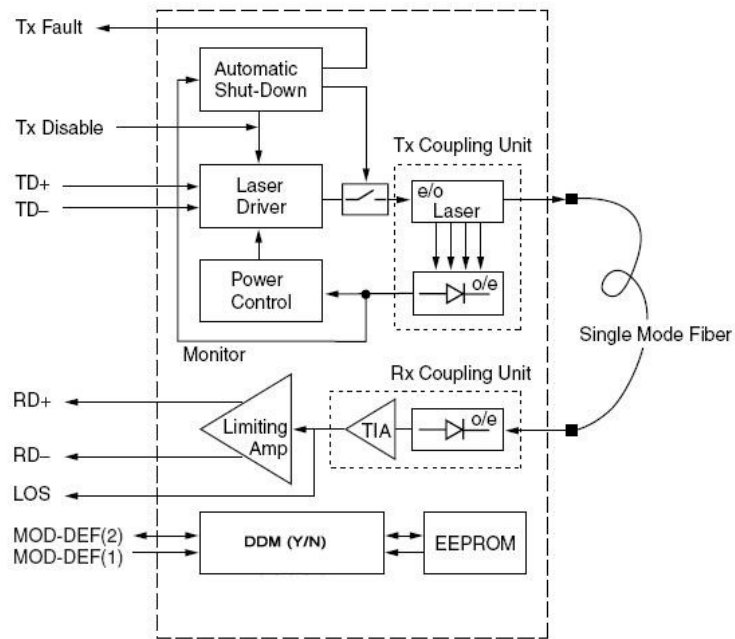
Note8: Eye Pattern Mask

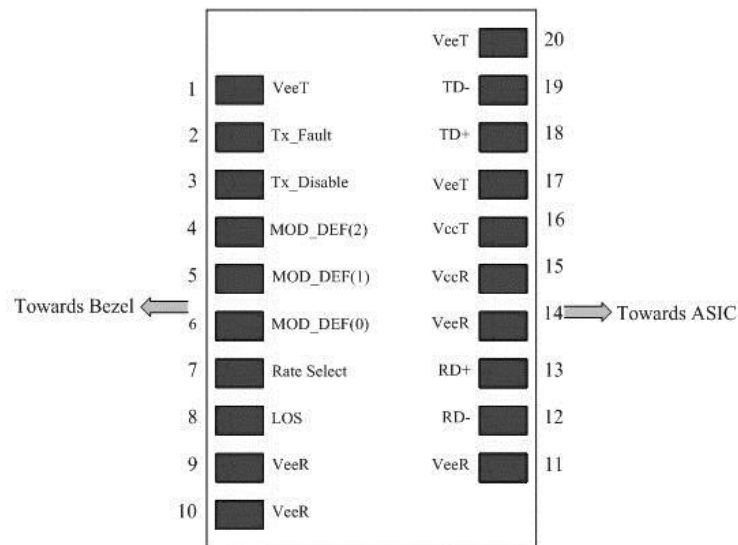
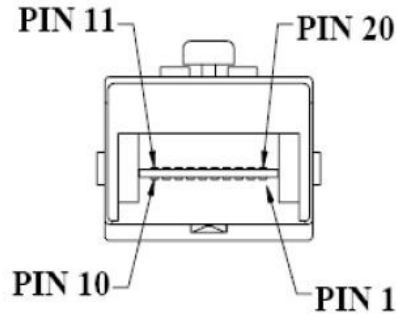


Note9: LOS Hysteresis



Functional Description of Transceiver



SFP Transceiver Electrical Pad Layout

Pin Function Definitions

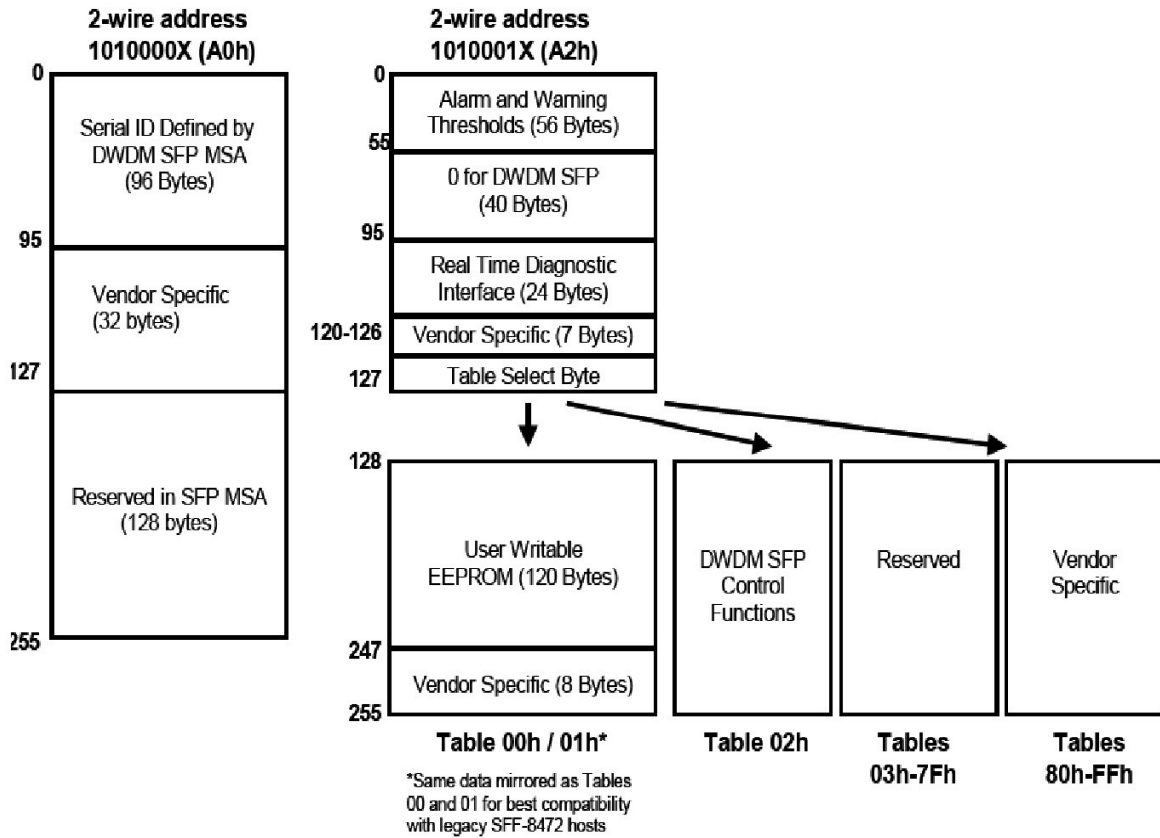
Pin Num.	Name	Function Description
1	VeeT	Transmitter Ground
2	TX Fault	Transmitter Fault Indication, open collector/drain output
3	TX Disable	Transmitter Disable
4	MOD-DEF2	Module Definition 2, Data line for Serial ID.
5	MOD-DEF1	Module Definition 1, Clock line for Serial ID.
6	MOD-DEF0	Module Definition 0, Grounded within the module.
7	Rate Select	Not Connect, Function not available
8	LOS	Loss of Signal, open collector/drain output
9	VeeR	Receiver Ground
10	VeeR	Receiver Ground
11	VeeR	Receiver Ground
12	RD-	Inv. Received Data Out
13	RD+	Received Data Out
14	VeeR	Receiver Ground
15	VccR	Receiver Power, 3.3 ± 5%
16	VccT	Transmitter Power, 3.3 ± 5%

17	VeeT	Transmitter Ground
18	TD+	Transmit Data In
19	TD-	Inv. Transmit Data In
20	VeeT	Transmitter Ground

EEPROM

The serial interface uses the 2-wire serial CMOS EEPROM protocol defined for the ATMEL AT24C02/04 family of components. When the serial protocol is activated, the host generates the serial clock signal (SCL). The positive edge clocks data into those segments of the EEPROM that are not write protected within the SFP transceiver. The negative edge clocks data from the SFP transceiver. The serial data signal (SDA) is bi-directional for serial data transfer. The host uses SDA in conjunction with SCL to mark the start and end of serial protocol activation. The memories are organized as a series of 8-bit data words that can be addressed individually or sequentially.

The Module provides diagnostic information about the present operating conditions. The transceiver generates this diagnostic data by digitization of internal analog signals. Calibration and alarm/warning threshold data is written during device manufacture. Received power monitoring, transmitted power monitoring, bias current monitoring, supply voltage monitoring and temperature monitoring all are implemented. If the module is defined as external calibrated, the diagnostic data are raw A/D values and must be converted to real world units using calibration constants stored in EEPROM locations 56 – 95 at wire serial bus address A2H. The digital diagnostic memory map specific data field define as following .For detail EEPROM information, please refer to the related document of SFF 8472 Rev 9.3.



EEPROM Serial ID Memory Contents

Accessing Serial ID Memory uses the 2 wire address 1010000X(A0H). Memory Contents of Serial ID are shown in Table 1.

Table 1 Serial ID Memory Contents

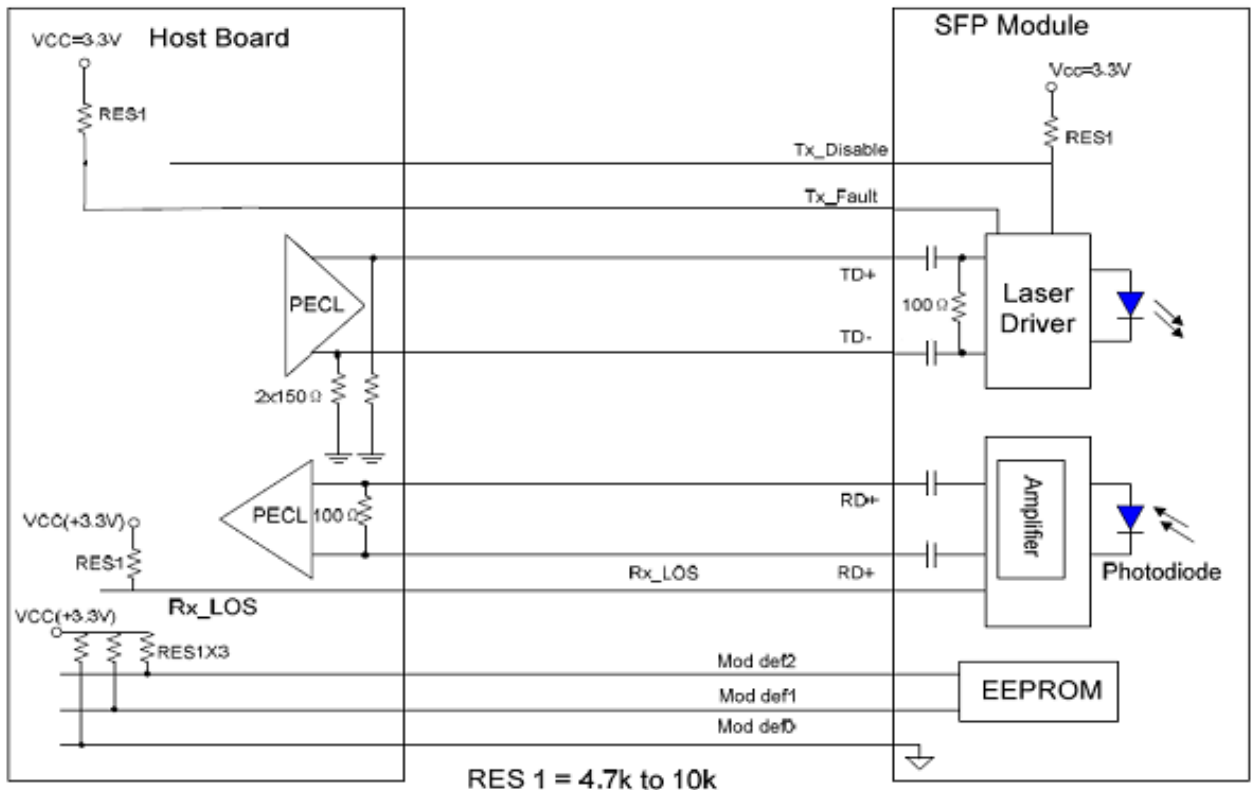
Addr.	Size(bytes)	Name of field	Vaule(Hex)	Description
0	1	Identifier	0B	DWDM SFP
1	1	Ext. Identifier	XX	
2	1	Connector	07	LC connector
3-10	8	Transceiver Codes	00	Reserved
			00	-
			00	-
			XX	
			XX	
			XX	
			01	Single mode
11	1	Encoding	XX	
12	1	BR, Nominal	0D	1.25Gbps
13	1	Reserved	00	-
14	1	Length (9µm)km	XX	
15	1	Length(9µm)100m	FF	

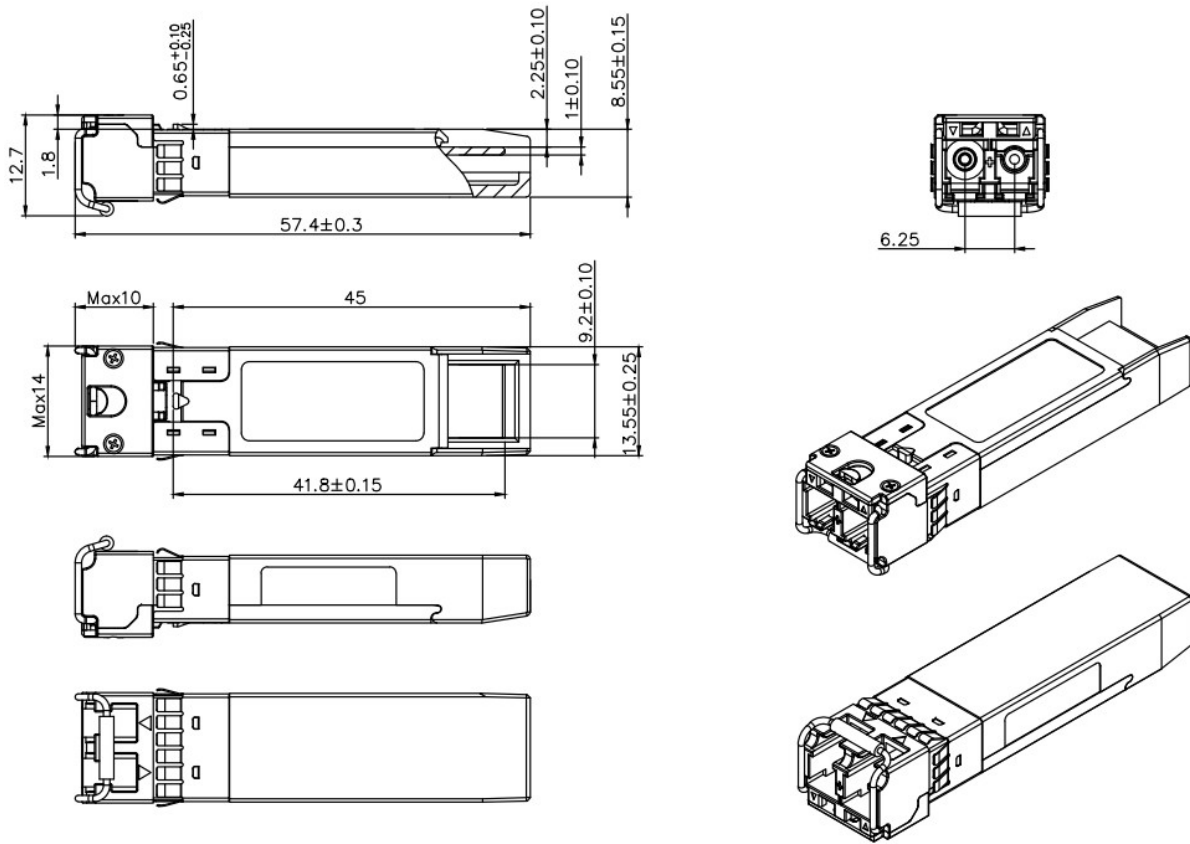
16	1	Length (50μm) 10m	00	
17	1	Length(62.5μm)10m	00	
18	1	Length (Copper)	00	Not compliant
19	1	Reserved	00	
20~35	16	Vendor Name	XX XX XX XX XX XX XX XX XX XX XX XX XX XX XX XX ^(note10)	Vendor name
36	1	Implemented Optional DWDM Features	00	-
37-39	3	Vendor OUI	00 00 00	-
40-55	16	Vendor PN	XX XX XX XX XX XX XX XX XX XX XX XX XX XX XX XX ^(note10)	PN
56-59	4	Vendor Rev	XX XX XX XX ^(note10)	
60-62	3	Wavelength	XX	Laser Wavelength
63	1	CC-BASE	XX	CC for Base ID fields implemented (addresses 0 to62)
64~65	2	Options	00	Reserved
			1A	1.TX_DISABLE is implemented and disables the serial output; 2.TX_FAULT signal implemented; 3.Loss of Signal implemented
66	1	BR, max	00	-
67	1	BR, max	00	-
68~83	16	Vendor SN	XX	Serial number of Transceiver (ASCII)
84~89	6	Date code	XX	The vendor's date code (ASCII)
90~91	2	Vendor specific lot code	XX XX	-
92	1	Diagnostic Monitoring Type	XX	1. Digital diagnostic monitoring implemented 2. Internally/Externally Calibrated;

				3.Received power measurement type is Average Power
93	1	Enhanced Options	F0	1.Optional Alarm/warning flags implemented for all monitored quantities 2. Optional Soft TX_DISABLE control and monitoring implemented 3. Optional Soft TX_FAULT monitoring Implemented 4. Optional Soft RX_LOS monitoring Implemented
94	1	SFF-8472 Compliance	01	Includes functionality described in Rev 9.3 of SFF-8472.
95	1	CC_EXT	XX	CC for the extended ID Fields (addresses 64 to 94) implemented.
96~127	32	Vendor Specific	XX	Read only memory
128-255	128	Reserved	Read only	

Note10: The "XX" byte should be filled in according to practical case. For more information, please refer to the related document of SFP Multi-Source Agreement (MSA).

Recommend Circuit Schematic



Mechanical Specifications


Unremarked tolerances $\pm 0.2\text{mm}$

*This 2D drawing only for reference, please check with Data Controls Inc before ordering.

Obtaining Document

You can visit our website: <https://www.dci.jp>

Or contact Data Controls Inc. Listed at the end of the documentation to get the latest documents.

Revision History

Revision	Revision History	Release Date
V2.a	Released.	Oct 24, 2009
V2.b	Revise the A2H.	Nov 24, 2009
V2.c	Complete the DWDM wavelength.	Jan 18, 2010
V2.d	Correct Rx λ range.	Apr 8, 2010
V3.a	Update PN&LOGO.	July 19, 2011
V3.b	Update EEPROM map, integrate 3 products.	Sep 20, 2011

V3.c	Add 32Db product.	Oct 17, 2011
V3.d	Add OSNR/DNP.	Feb 22,2012
V3.e	Update regulatory compliance,lcc, Tr/Toff and the Pout of 37Db products.	Sep 25,2013
V3.f	Add CPRI application. Update regulatory compliance, LOSA of 24Db/28Db and the tolerance of mechanical spec.	Mar 03,2015
V3.g	Update the tolerances of mechanical spec.	April 09,2015
V3.h	Add the 41dB product information.	April 22,2015
V3.i	Update the regulatory compliance and add note forpicture and 2D drawing.	Jan 5,2016
V3.j	Add the industrial temperaturerange, update the laser type, the label picture and 2D drawing	Feb 9,2017

Notice:

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Contact:

Add: 8F, Fukashiro Bldg. 1-20-4 Yanagibashi, Taito-ku, Tokyo 111-0052 Japan

Tel: +81-3-5829-5805 FAX: +81-3-5829-5806

E-mail: info@dcj.jp <https://www.dci.jp>