


Features

- Electrical interface compliant to QSFP+ connector (SFPP-8436) and SFP+ connectors (SFF-8431)
- 850nm VSCSEL transmitter, PIN photo-detector receiver
- Hot Pluggable
- Up to 100m on OM3 MMF
- Operating case temperature : 0~70°C
- All-metal housing for superior EMI performance
- RoHS Compliant

Applications

- Infiniband SDR, DDR and QDR
- 40G Ethernet (40GbE)
- Fiber Channel Applications
- Switch and router high speed backplane interconnect
- High performance computing clusters.

Absolute Maximum Ratings

Parameter	Symbol	Min.	Max.	Units	Note
Storage Temperature	T_s	0	85	°C	
Storage Ambient Humidity	H_A	0	85	%	
Power Supply	V_{CC3}	0	3.6	V	+3.3V

Recommended Operating Conditions

Parameter	Symbol	Min.	Max.	Units	Note
Case Operating Temperature	T_C	0	70	°C	
Ambient Humidity	H_A	5	85	%	Non-Condensing
Supply Voltage	V_{CC3}	3.135	3.465	V	
Supply Current	I_{CC3}	---	400	mA	
Total Power Dissipation	P_D	---	0.85	W	QSFP+
	P_D	---	0.85	W	SFP

Optical Characteristics– Transmitter (QSFP+)

<i>Parameter</i>	<i>Symbol</i>	<i>Min</i>	<i>Type</i>	<i>Max</i>	<i>Units</i>	<i>Notes</i>
Center Wavelength	λ_t	840	850	860	nm	
RMS spectral width	$\Delta\lambda$	-	-	0.65	nm	
Average Optical Power	Pout	-7.5	-	2.5	dBm	
Difference in launch power between any two lanes (OMA)				4	dB	
Extinction Ratio	ER	3	-	-	dB	
Peak power, each lane				4	dBm	
Transmitter Dispersion Penalty	TDP	-	-	3.5	dB	
Average launch power of OFF transmitter, each lane		-	-	-30	dB	
Eye Mask coordinates: X1, X2, X3, Y1, Y2, Y3						Specification Values 0.23, 0.34, 0.43, 0.27, 0.35, 0.4 Hit Ration=5x10-5

High Speed Characteristics – Receiver (QSFP+)

<i>Parameter</i>	<i>Symbol</i>	<i>Min</i>	<i>Type</i>	<i>Max</i>	<i>Units</i>	<i>Notes</i>
Center Wavelength	λ_r	840	850	860	nm	
Stressed Sensitivity in OMA(each lane)		-	-	-5.4	dBm	Note 1
Maximum Average power at receiver input				2.4	dBm	
Peak power, each lane				4	dBm	
LOS Assert		-30	-		dBm	
LOS De-Assert-OMA				-7.5	dBm	
LOS Hysteresis		0.5	-		dB	
Receiver Reflectance		-	-	-12	dB	

Notes:

1. Measured with conformance test signal at TP3 for, BER=10e⁻¹²

Optical Characteristics-Transmitter (SFP+)

<i>Parameter</i>	<i>Symbol</i>	<i>Min</i>	<i>Type</i>	<i>Max</i>	<i>Units</i>	<i>Notes</i>
Center Wavelength	λ_t	840	850	860	nm	
RMS spectral width	Pm	-	-	Note 1	nm	
Average Optical Power	Pavg	-6.5	-	-1	dBm	Note 2
Extinction Ratio	ER	3.5	-	-	dB	Note 3
Transmitter Dispersion Penalty	TDP	-	-	3.9	dB	
Relative Intensity Noise	Rin	-	-	-128	dB/ Hz	12dB reflection
Optical Return Loss Tolerance		-	-	12	dB	

Note:

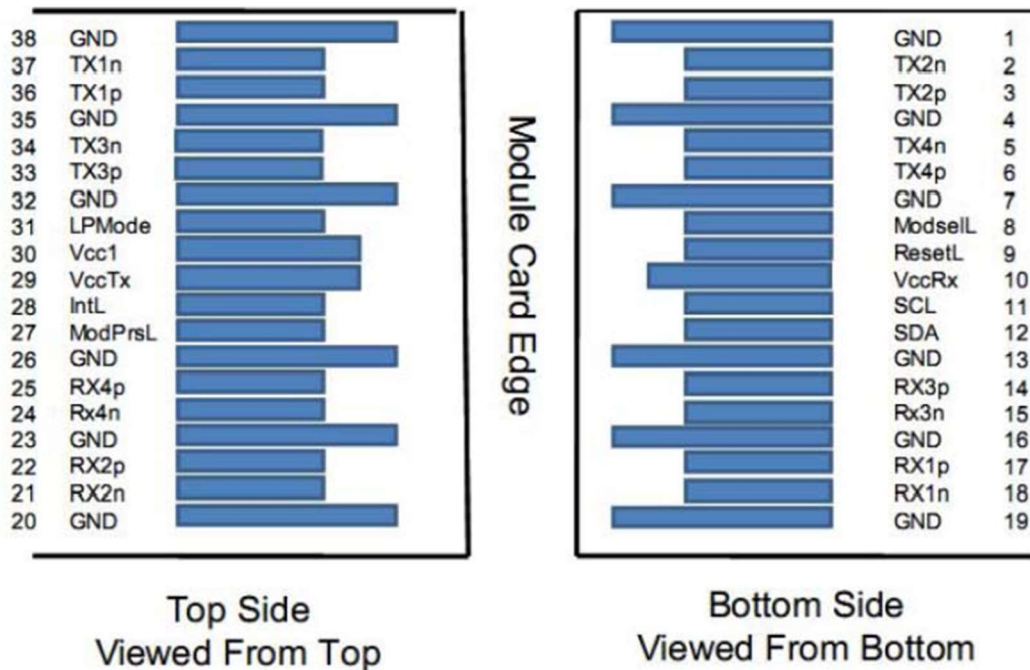
1. Trade-offs are available between spectral width, center wavelength and minimum OMA, as shown in table 6
2. The Optical power is launched into MMF
3. Measured with a PRBS 2³¹-1 test pattern @10.3125Gbps

Optical Characteristics – Receiver (SFP+)

<i>Parameter</i>	<i>Symbol</i>	<i>Min</i>	<i>Type</i>	<i>Max</i>	<i>Units</i>	<i>Notes</i>
Center Wavelength	λ_r	840	850	860	nm	
Receiver Sensitivity	Psens	-	-	-11.1	dBm	Note 1
Stressed Sensitivity in OMA		-	-	-7.5	dBm	Note 1
Los function	Los	-30	-	-12	dBm	
Overload	Pin	-	-	-1.0	dBm	Note 1
Receiver Reflectance		-	-	-12	dB	

Notes:

1. Measured with a PRBS 2³¹-1 test pattern @10.3125Gbps, BER $\leq 10^{-12}$

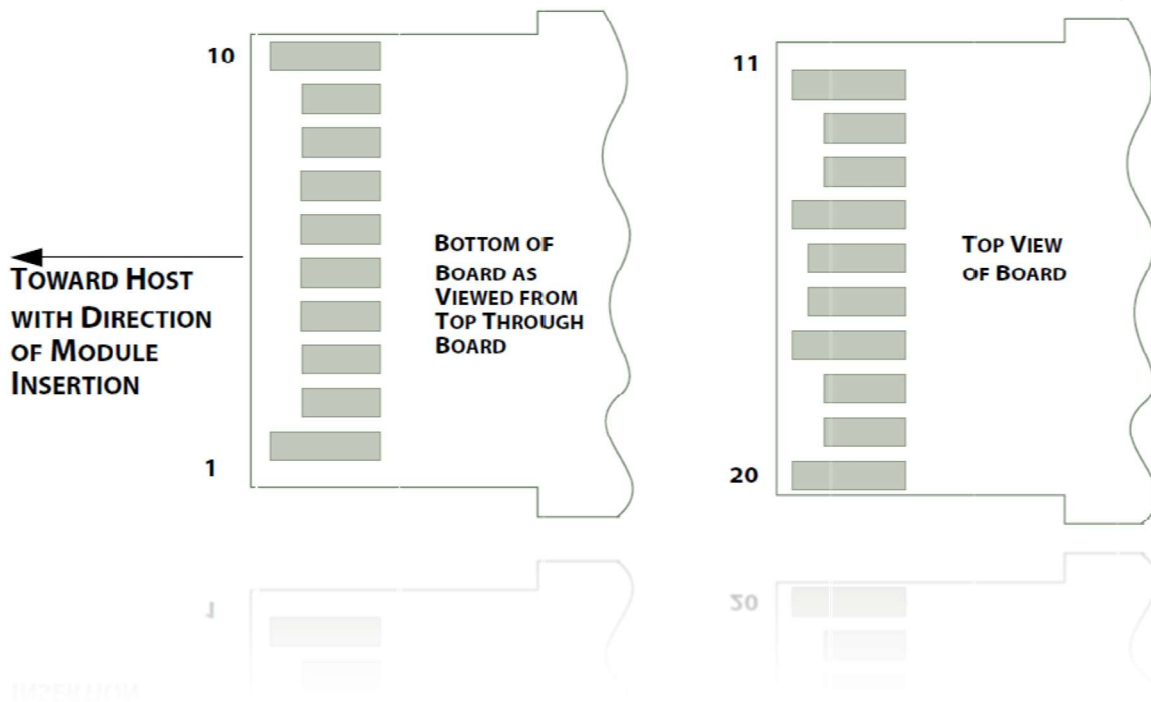
Pin Assignment (QSFP+)
QSFP Module Pad Layout (Top View)

Pin Description

Pin	Logic	Symbol	Description	Note
1		GND	Ground	1
2	CML-I	Tx2n	Transmitter Inverted Data Input	
3	CML-I	Tx2p	Transmitter Non-inverted Data Input	
4		GND	Ground	1
5	CML-I	Tx4n	Transmitter Inverted Data Input	
6	CML-I	Tx4p	Transmitter Non-inverted Data Input	
7		GND	Ground	1
8	LVTTL-I	ModSeIL	Module Select	
9	LVTTL-I	ResetL	Module Reset	
10		VccRx	+3.3V Power Supply Receiver	
11	LVC MOS-I/O	SCL	2-Wire Serial Interface Clock	2
12	LVC MOS-I/O	SDA	2-Wire Serial Interface Data	2
13		GND	Ground	1

Pin	Logic	Symbol	Description	Note
14	CML-O	Rx3p	Receiver Non-Inverted Data Output	
15	CML-O	Rx3n	Receiver Inverted Data Output	
16		GND	Ground	1
17	CML-O	Rx1p	Receiver Non-Inverted Data Output	
18	CML-O	Rx1n	Receiver Inverted Data Output	
19		GND	Ground	1
20		GND	Ground	1
21	CML-O	Rx2n	Receiver Inverted Data Output	
22	CML-O	Rx2p	Receiver Non-Inverted Data Output	
23		GND	Ground	1
24	CML-O	Rx4n	Receiver Inverted Data Output	
25	CML-O	Rx4p	Receiver Non-Inverted Data Output	
26		GND	Ground	1
27	LVTTL-O	ModPrsL	Module Present	2
28	LVTTL-O	IntL	Interrupt	2
29		VccTx	+3.3V Power Supply Transmitter	
30		Vcc1	+3.3V Power Supply	
31	LVTTL-1	LPMODE	Low Power Mode	
32		GND	Ground	1
33	CML-I	Tx3p	Transmitter Non-Inverted Data Input	
34	CML-I	Tx3n	Transmitter Inverted Data Input	
35		GND	Ground	1
36	CML-I	Tx1p	Transmitter Non-Inverted Data Input	
37	CML-I	Tx1n	Transmitter Inverted Data Input	
38		GND	Ground	1

Notes:

- GND is the symbol for signal and supply (power) common for QSFP modules. All are common within the QSFP module and all module voltages are referenced to this potential unless otherwise noted. Connect these directly to the host board signal common ground plane.
- VccRx, Vcc1 and VccTx are the receiver and transmitter power suppliers and shall be applied concurrently. Recommended host board power supply filtering is shown below. Vcc Rx, Vcc1 and Vcc Tx may be internally connected within the QSFP transceiver module in any combination. The connector pins are each rated for a maximum current of 500mA.

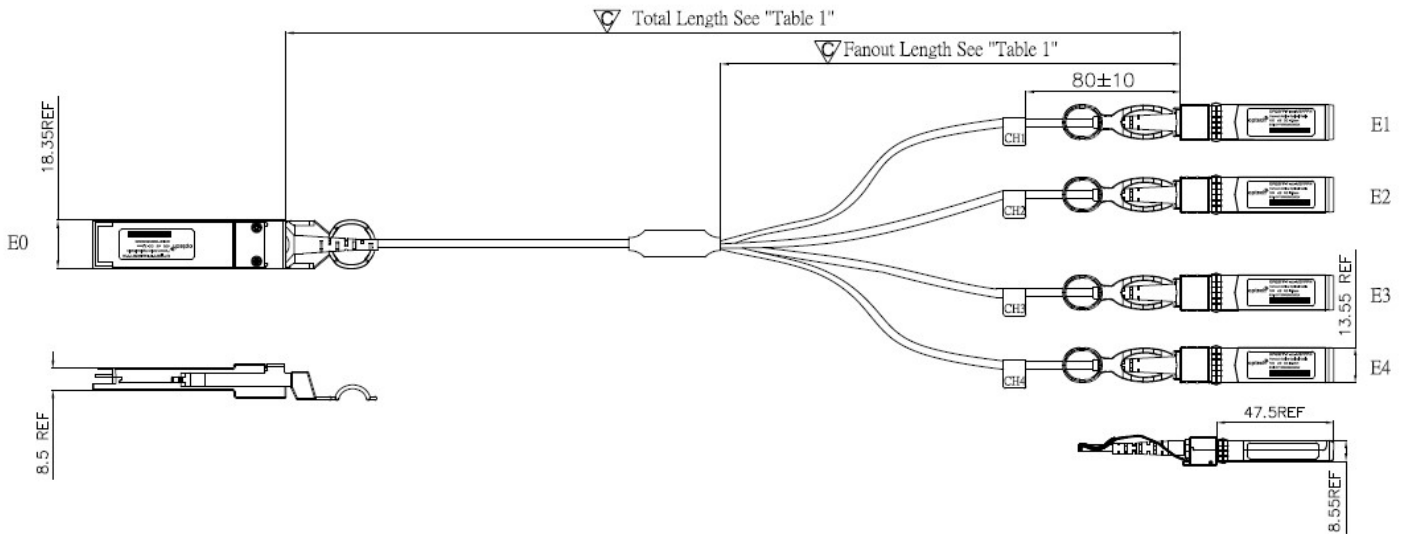
Pin Assignment (SFP+)


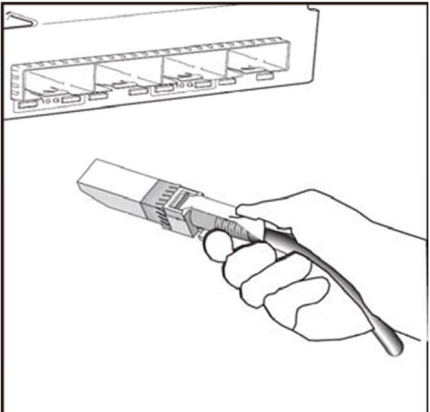
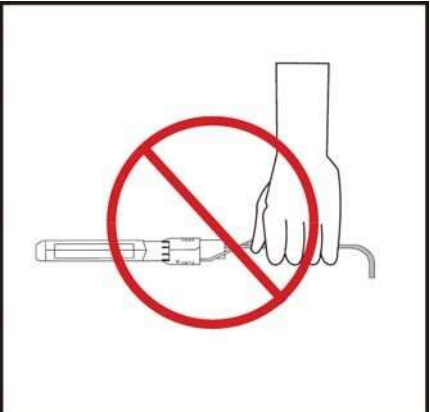
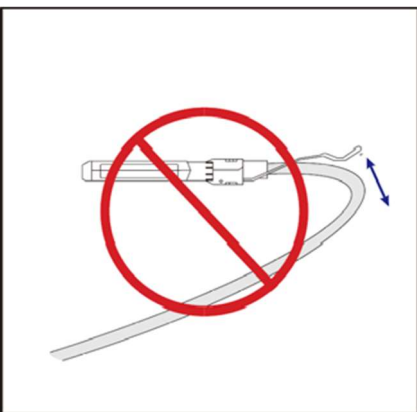


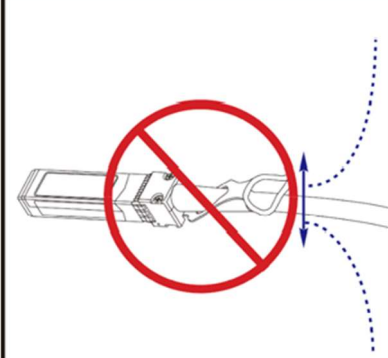
Contacts	Logic1	Symbol	Power Sequence Order	Name/Description	Note
1		VeeT	1 st	Module transmitter ground	3
2	LVTTTL-O	Tx_Fault	3 rd	Module transmitter fault	4
3	LVTTTL-I	TX_Disable	3 rd	Transmitter disable Turns Off transmitter laser output	5
4	LVTTTL-I/O	SDA	3 rd	2-wire serial interface data line (same as MOD-DEF2 in INF-8074i)	6
5	LVTTTL-I/O	SCL	3 rd	2-wire serial interface clock (same as MOD-DEF1 in INF-8074i)	6
6		Mod_ABS	3 rd	Module absent connected to VeeR or VeeR in the module	7
7	LVTTTL-I	RS0	3 rd	Rate Select 0, optionally controls SFP+ module receiver	8
8	LVTTTL-O	Rx_LOS	3 rd	Receiver loss of signal indication (In fc designated as RX_LOS and in Ethernet designated as signal detec)	4

Contacts	Logic1	Symbol	Power Sequence Order	Name/Description	Note
9	LVTTL-I	RS1	3 rd	Rate Select 1, optionally controls SFP+ module transmitter	8
01		VeeR	1 st	Module receiver ground	3
11		VeeR	1 st	Module receiver ground	3
12	CML-O	RD-	3 rd	Receiver inverted data output	
13	CML-O	RD+	3 rd	Receiver non-inverted data output	
14		VeeR	1 st	Module receiver ground	3
15		VccR	2 nd	Module receiver 3.3 V supply	
16		VccT	2 nd	Module transmitter 3.3 V supply	
17		VeeT	1 st	Module transmitter ground	3
18	CML-I	TD+	3 rd	Transmitter non-inverted data input	
19	CML-I	TD-	3 rd	Transmitter inverted data input	
20		VeeT	1 st	Module transmitter ground	3

Notes

- Labeling as inputs (I) and outputs (O) are from the perspective of the module
- The case makes electrical contact to the cage before any of the board edge contacts are made.
- The module signal ground contacts, VeeR and VeeT, should be isolated from the module case.
- This contact is an open collector/drain output contact and shall be pulled up on the host see SFF-8431 2.4.1 and SFF-8431 2.4.6. Pull ups can be connected to one of several power supplies, however the host board design shall ensure that no module contact has voltage exceeding module VccT/R + 0.5V.
- Tx_Disable is an input contact with a 4.7 kΩ to 10 kΩ pullup to VccT inside the module.
- See SFF-8431 4.2.
- See SFF-8431 2.4.4.
- For SFF-8431 rate select definition see section SFF-8431 2.4.3 and SFF-8431 2.5. (If implementing SFF-8079 contact 7 and 9 in SFF-8431 are used for AS0 and AS1 respectively). Mechanical specifications:

Dimensions

Important Notice

		
Holding the SFP+ connector by its sides, insert the connector into the port on the switch	Do not handle by cable	DO NOT Over-bend the cable behind the connector
		
DO NOT twist the cable	DO NOT kink the cable	DO NOT bend up and down the cable

Ordering Information
AOC-OM3-QSFP / 4SFP+ - XX M

<u>Length: User Specify</u>		
1: 1M	3: 3M	5: 5M
7: 7M	10: 10M	30: 30M

<i>Part Number</i>	<i>Model Number</i>	<i>Length (M)</i>	<i>Voltage</i>	<i>Temperature</i>
AOC-OM3-QSFP/4SFP+-1M	Active Optical Cable	1	3.3V	0°C ~ 70°C
AOC-OM3-QSFP/4SFP+-3M	Active Optical Cable	3	3.3V	0°C ~ 70°C
AOC-OM3-QSFP/4SFP+-5M	Active Optical Cable	5	3.3V	0°C ~ 70°C
AOC-OM3-QSFP/4SFP+-7M	Active Optical Cable	7	3.3V	0°C ~ 70°C
AOC-OM3-QSFP/4SFP+-10M	Active Optical Cable	10	3.3V	0°C ~ 70°C
AOC-OM3-QSFP/4SFP+-20M	Active Optical Cable	20	3.3V	0°C ~ 70°C
AOC-OM3-QSFP/4SFP+-30M	Active Optical Cable	30	3.3V	0°C ~ 70°C
AOC-OM3-QSFP/4SFP+-40M	Active Optical Cable	40	3.3V	0°C ~ 70°C
AOC-OM3-QSFP/4SFP+-50M	Active Optical Cable	50	3.3V	0°C ~ 70°C

Note: All information contained in this document is subject to change without notice.